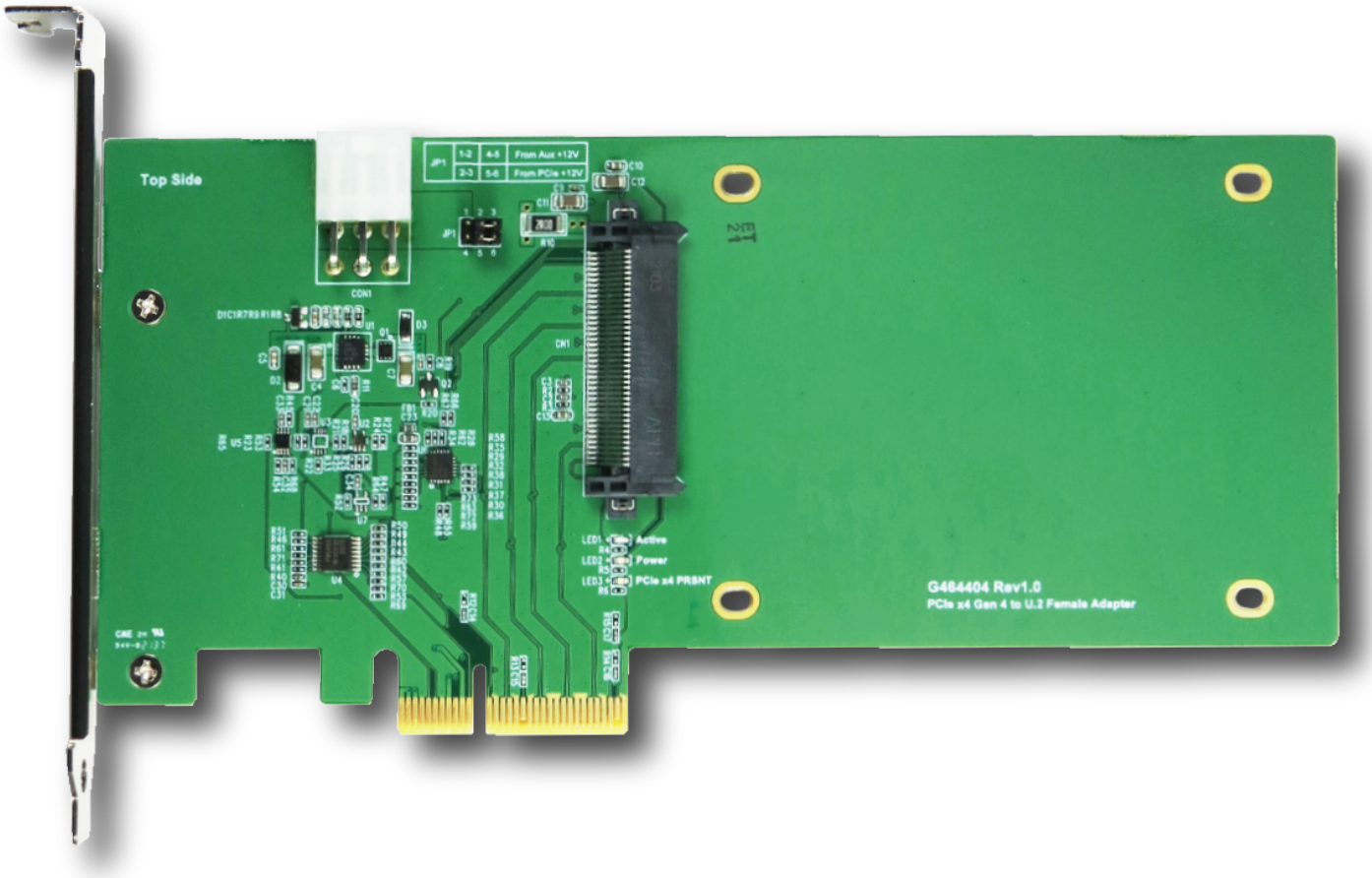
Innocard

Minerva

DP9105

PCIe x4 Gen4 to U.2 (SFF-8639) AIC



PCIe x4 Gen4 to U.2(SFF-8639) AIC

Features

※ Supports Server BMC management
※ Supports Industrial Grade temerature range
※ U.2 (SFF-8639) to PCI Express 4.0 x4 convert
※ Built- in SFF-8639, 30u"connector
※ Built- in PCIe 100MHz Clock buffer to drive longer trace lengths and longer cable, Address: 0x6C(7 bits)
※ Built- in SMBus Switch, Address: 0x71(7 bits)
※ Built- in SMBus I/O Expander, Address: 0x41(7 bits) for U.2 SSD PWRDIS control
※ Supports PCIe PERST# for OOB(out of band) management to remote U.2 SSD Reset
※ Built- in WAKE# bidirectional voltage-level translator for Open-Drain output to be used over longer trace lengths and over longer cable - Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II
※ Built- in CLKREQ# bidirectional voltage-level translator for Open-Drain output to be used over longer trace lengths and longer over cable - Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II
※ Built-in two 12V Hot Plug Power with eFuse protection for U.2 dual port
  ◆Programmable Current Limit: 6.5A
  ◆Programmable Inrush Current Slew Rate
  ◆Thermal shutdown protection and fault alert
  ◆Back-to-Back (B2B) FET Operation
※ Built-in 12V output with 30-V, N channel NexFET power MOSFET, 32 m Ohm
※ Built-in 12V output with SCHOTTKY BARRIER Power Rectifier

Specifications

※ PCI Express Base Specification Rev 4.0
※ PCIe_CEM_SPEC_R4_V1_0_08072019_NCB
※ Support SSD_Form_Factor_Version1_a
※ PCIe_SFF_R4.0_V1.0_03112021_NCB
※ Compliant with SFF-TA-1001 Specification Version 1.1

Operating system support

※ Windows 7
※ Windows 8 &8.1
※ Windows 10
※ UEFI 2.3.1 or later

Applications

※ Rack server
※ Microserver and Tower server
※ High performance computing
※ Hareware accelerator
※ Storage Controller HBA(Host Bus Adapter)
※ Desktop PC/motherboard

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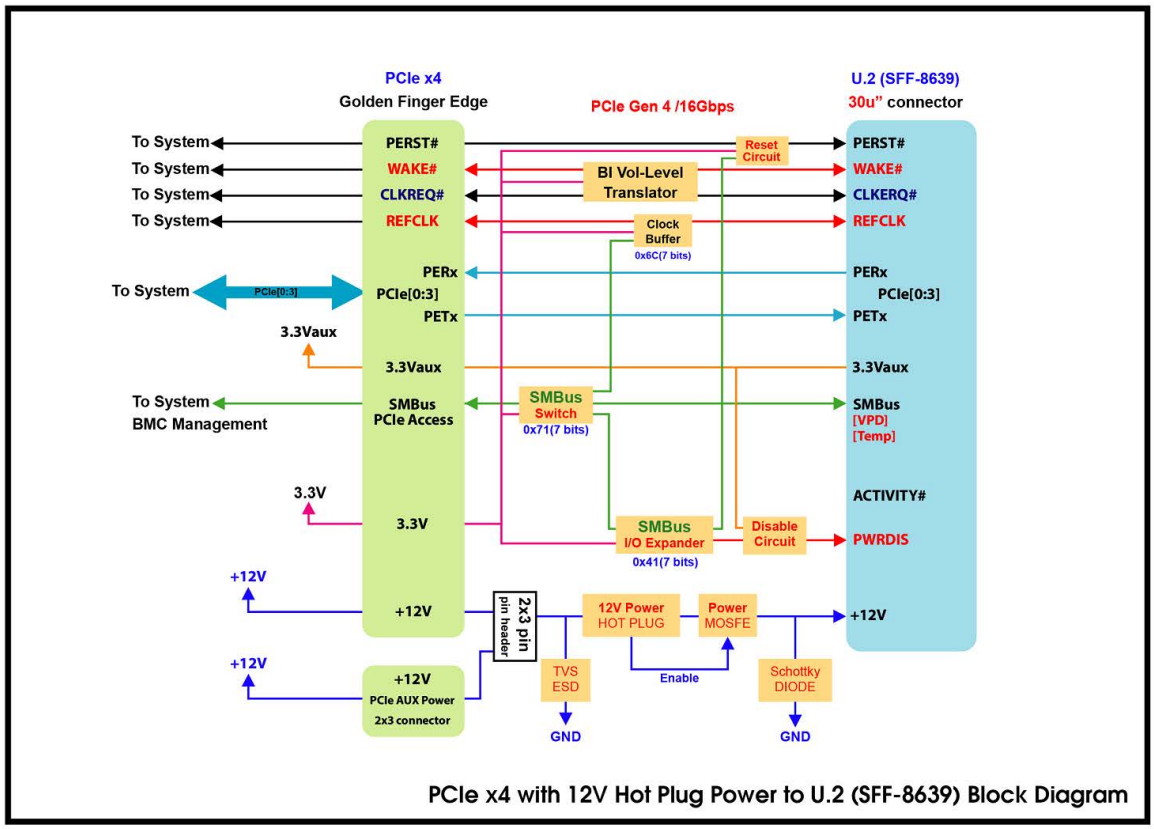

PCIe x4 with 12V Hot Plug Power to U.2 (SFF-8639) Block Diagram

DP9105 Jumper Application